



Politechnika Wroclawska



Electronic Circuits

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 WEB: <http://156.17.38.202>

Recording and publishing lectures and
 other activities
is strictly forbidden !




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To pass the course:

- **positive grade** of „the project”
- If necessary, additional test possible by the end of semester
- **positive grade** of „the lab”
- **positive grade** of the final TEST
- zero try - last week of the semester
- The 1th and 2nd test - after semester


• **FINAL GRADE of COURSE:**
 Average of the above (when in doubts closer „TEST”)



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References:

1. W.Tietze, Ch.Schenk, E.Gamm, **Electronic Circuits. Handbook for Design and Application**, Springer 2nd Edition
2. P. Horowitz, W. Hill, **The Art. Of Electronics**, Cambridge University Press 2015
3. C. Kitchin, L. Counts, **A designer's guide to instrumentation amplifier**, 3rd edition, Analog Devices
4. Additional references cited during lectures



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Warning !

The slides are a supporting element of the lecture (the guide).

The slides do not constitute the source of knowledge.